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02/03/2004



Alfred A. Equitz
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Marsha A. Townsend	(Depositor's name)
<i>Marsha A. Townsend</i>	(Signature)
February 13, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/006,334	12/03/2001	Kamesh V. Gadepally	NSCI-G0610 [P04402 P01]	3399

TITLE OF INVENTION: METHOD FOR MANUFACTURING AN INTEGRATED CIRCUIT STRUCTURE WITH LIMITED SOURCE SALICIDATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	05/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
HOANG, QUOC DINH	2818	438-592000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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NATIONAL SEMICONDUCTOR CORPORATION

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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Alfred A. Equitz, Reg. No. 30,922

2/13/04

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